

**IN THE CLAIMS**

Please substitute claims 1-13 with the following:

1. (Currently Amended) A semiconductor device comprising:

a first buried wiring;

a second buried wiring formed as a layer different from said first buried wiring;

a contact hole, which is formed between said first buried wiring and said second buried wiring and is filled with a wiring material ~~for~~ electrically connecting said first buried wiring and said second buried wiring therethrough; and

a dummy hole, which has a hole diameter different from that of said contact hole, is so formed in the vicinity of said contact hole ~~as to connect said first buried wiring so that a surface of said first buried wiring is exposed to said dummy hole,~~ and is filled with a wiring material therein.

2. (Original) The semiconductor device according to claim 1, wherein said second buried wiring is formed as an upper layer of said first buried wiring, and said dummy hole is formed over said first buried wiring and has a diameter larger than said contact hole.

3. (Original) The semiconductor device according to claim 1, wherein said second buried wiring is formed as an upper layer of said first buried wiring, and said dummy hole is formed below said first buried wiring and has a diameter smaller than said contact hole.

4. (Original) The semiconductor device according to claim 1, wherein said second buried wiring is formed as a lower layer of said first buried wiring, and said dummy hole is formed below said first buried wiring and has a diameter smaller than said contact hole.

5. (Original) The semiconductor device according to claim 1, wherein said second buried wiring is formed as an upper layer of said first buried wiring, and said dummy hole is formed over said first buried wiring and has a diameter smaller than said contact hole.

6. (Original) The semiconductor device according to claim 1, wherein said wiring material is made of copper.

7. (Original) The semiconductor device according to claim 3, wherein a diameter of said dummy hole is so set as to cause a plugging failure when said dummy hole is filled with said wiring material.

8. (Original) The semiconductor device according to claim 4, wherein a diameter of said dummy hole is so set as to cause a plugging failure when said dummy hole is filled with said wiring material.

9. (Original) The semiconductor device according to claim 5, wherein a diameter of said dummy hole is so set as to cause a plugging failure when said dummy hole is filled with said wiring material.

10. (Currently Amended) A method for manufacturing a semiconductor device including a first buried wiring, a second buried wiring formed as an upper layer of said first buried wiring, a contact hole, which is formed between said first buried wiring and said second buried wiring and is filled with a wiring material for permitting electric connection between said first buried wiring and said second buried wiring therewith, and a dummy hole, which is so formed over said first buried wiring in the vicinity of said contact hole and has a hole diameter larger than said contact hole and in which a wiring material is filled, the method comprising the steps of:

forming said first buried wiring;

forming an insulating film on said first buried wiring through a diffusion-preventive film and etching the insulating film to simultaneously form said contact hole and said dummy hole in said insulating film so that a surface of said first buried wiring is exposed to at a bottom of said dummy hole; and filling said contact hole and said dummy hole with a wiring material, respectively.

11. (Currently Amended) A method for manufacturing a semiconductor device including a first buried wiring, a second buried wiring formed as an upper layer of said first buried wiring, a contact hole, which is formed between said first buried wiring and said second buried wiring and is filled with a wiring material for permitting electric connection between said first buried wiring and said second buried wiring therewith, and a dummy hole, which is formed below said first buried wiring in the vicinity of said contact hole and has a hole diameter smaller

than said contact hole and in which a wiring material is filled, the method comprising the steps of:

forming a wiring groove for forming said first buried wiring and forming said dummy hole below the wiring groove in such a size as to cause a plugging failure when a wiring material is filled in the dummy hole;

burying said dummy hole and said wiring groove with a wiring material; and  
forming the contact hole over said first buried wiring that has been formed by the burying of the wiring material and filling said contact hole with a wiring material.

12. (Original) A method for manufacturing a semiconductor device including a first buried wiring, a second buried wiring formed as a lower layer of said first buried wiring, a contact hole, which is formed between said first buried wiring and said second buried wiring and is filled with a wiring material for electrically connecting said first buried wiring and said second buried wiring therewith, and a dummy hole, which is formed below said first buried wiring in the vicinity of said contact hole and has a hole diameter smaller than said contact hole and into which a wiring material is filled, the method comprising the steps of:

forming said second buried wiring;

forming said contact hole and said dummy hole over said second buried wiring in such a size as to cause a plugging failure when a wiring material is filled in said dummy hole;

forming a groove for wiring for forming said first buried wiring in such a condition as to be electrically connected with said contact hole and said dummy hole; and

filling a wiring material into the contact hole, the dummy hole, and the groove for wiring, respectively.

13. (Original) A method for manufacturing a semiconductor device including a first buried wiring, a second buried wiring formed as an upper layer of said first buried wiring, a contact hole, which is formed between said first buried wiring and said second buried wiring and is filled with a wiring material for electrically connecting said first buried wiring and said second buried wiring therewith, and a dummy hole, which is formed over said first buried wiring in the vicinity of said contact hole and has a hole diameter smaller than said contact hole and in which a wiring material is filled, the method comprising the steps of:

forming said first buried wiring;

forming said contact hole and a dummy pattern over said first buried wiring in such a size as to cause a plugging failure when a wiring material is filled in said dummy hole;

forming a groove for wiring over said first buried wiring for forming said second buried wiring in such a condition as to be electrically connected with said contact hole and said dummy pattern; and

filling a wiring material in said contact hole, said dummy hole, and said groove for wiring, respectively.

14. (New) A semiconductor device comprising:

a first buried wiring;

a second buried wiring formed as a layer different from said first buried wiring;

a contact hole formed between said first buried wiring and said second buried wiring and filled with a wiring material for electrically connecting said first buried wiring and said second buried wiring therethrough; and

a dummy hole formed in proximity to said contact hole and to have a hole diameter different from that of said contact hole so that a surface of said first buried wiring is exposed to said dummy hole, the dummy hole being filled with a wiring material therein such that the dummy hole is operatively configured to inhibit a void from being generated in said contact hole when said first buried wiring is under stress.